

J&R-0694

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Thomas Zettler
Signature

October 31, 2005
Date

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 09/922,479 Confirmation No. 7212
Applicant : Thomas Zettler
Filed : August 3, 2001
Title : Method and Device for Testing an Integrated
Circuit, Integrated Circuit to be Tested, and
Wafer with a Large Number of Integrated
Circuits to be Tested
Group Art Unit : 2133
Examiner : Phung M. Chung
Docket No. : J&R-0694
Customer No. : 24131

A M E N D M E N T

Mail Stop Amendment

Hon. Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

S i r :

Responsive to the Office Action dated August 30, 2005, kindly
amend the above-identified application as follows:

Amendments to the Claims begin on page 2 of this paper.

Remarks/Arguments begin on page 12 of this paper.

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